

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLN. OF: TAKANO et al.

SERIAL NO.: 10/648,995

FILED: August 27, 2003

FOR: Semiconductor Integrated Circuit Having Logic Circuit Comprising...

DOCKET: NEC OSP-9770 DIV II

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In connection with the above-entitled matter, Applicants hereby proffer U.S. Patent Office Form PTO-1449 including the references listed therein. The present application is a divisional of application Serial No. 10/357,752, wherein the references referred to in the enclosed U.S. Patent Office Form PTO-1449 have been cited. Enclosed also is a copy of the U.S. Examiner's Action in which the references were cited. The claims in the present application are believed to be patentably distinguished over these references.

This Supplemental Information Disclosure Statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the information thus disclosed in fact constitutes prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.

In compliance with the requirements of 37 C.F.R. § 1.98(a)(3), as a concise statement of relevance, as it is presently understood by the individual designated in 35 U.S.C. § 1.56(c) most knowledgeable about the content of the information. The undersigned attorney of record submits an official action by a U.S. examiner in which the references were cited. The relevance to the

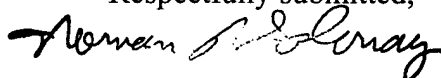
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pending U.S. patent application is that the references were cited in the parent patent application on the same subject matter. However, no independent analysis of the references, the accuracy of the statement of the PTO examiner relative to the subject matter claimed in the present application has been made; the present understanding of the contents thereof by the undersigned being based on the examiner's comments submitted therewith.

The enclosed Supplemental Information Disclosure Statement is being submitted prior to issuance of the first action on the merits. Therefore, we believe there are no fees involved with this Information Disclosure Statement. In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.


Respectfully submitted,



Norman P. Soloway
Attorney for Applicants
Reg. No. 24,315

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 6, 2004 at Tucson, Arizona.

By: 

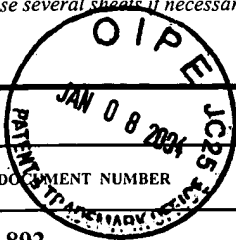
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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

NEC OSP-9770 DIV II

Application Number

10/648,995

Applicant(s)

TAKANO et al.

Filing Date

August 27, 2003

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,545,892	4.8.03	TAKANO et al.	365	51	
		5,138,194	8.11.92	YOELI	307	443	
		5,386,153	1.31.95	VOSS et al.	326	34	
		5,187,388	2.16.93	MOY	307	448	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		09-200036	7.31.97	JAPAN (w/abstract)	H03K	19/08		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and no considered. Include copy of this form with next communication to applicant.